

Hi all, welcome to The Hive's series on PCB Design with KiCAD. My name is Ben, and in this series, we've been walking through the PCB design process using KiCAD as our electronics design software.

The previous videos went through the design process all the way through, resulting in a complete PCB ready for fabrication. One thing that I mentioned during that process, and was featured in the original "EDA Design Flow" in part 2, was library management, and the idea of using only project-scope libraries, but when we actually did the design, I ignored this for simplicity and time-constraints.

Part 6 went through a project-specific symbol library.

In parts 7A and B, we've made a custom library and a custom footprint using the "blank slate" method.

In this video, we'll use the wizard to make a footprint for our IC, then decide never to repeat that if possible.

This material is of course not required for a functional design, but it is good design

practice, for KiCAD at least, to keep all your parts in a project-level library.

Because this is not related directly to the design flow of the previous videos, I'll make no assumptions about the state of your system or knowledge. So I apologize if some of this is repetition for some of you.

Let's get started.



As a review, this is where we left the last video, with seven of the required eight footprints in our project-specific library. Obviously, this battery holder was downloaded, not made by me, so if you saved your personal copy rather than the downloaded one, first, kudos to you, but second, it'll obviously look different unless you spent your precious time making this, in which case, your employer will not be happy. But I digress.

Footprint Library	
Finally, onto the IC.	
ICs are usually either a standard package with a known footprint, or based off a standard package, <i>but always</i> <i>confirm the supplier-provided package with the</i> <i>datasheet!</i>	
KiCAD has a large selection of standard footprints that *should* work. So the first place to check for the footprint is in KiCAD itself with the filter box in the Footprint Editor. Filter for the part number and the package name.	











Because SOIC stands for "Small Outline Integrated Circuit" and is the overarching family that includes SOT package types, among others.















This is a mockup of what the footprint parameters are. The row-spacing is measured center-to-center, but the pad interior edge-to-edge is smaller than the minimum body width, and the maximum package length is shorter than the pad exterior edge-to-edge.

Still doesn't answer the key question of what the pad length should be.

- Some datasheets are better about giving useful dimensions, or even better, a recommended footprint (like the battery holder did).
- Standard packages that are *not* in KiCAD still have a standard footprint that you can search for and use.
- Otherwise, you'll have to make your best judgement.
- You might be getting a sense for why companies should do this for you.
- Given all this, we're not actually going to make this part. It's not worth our time.

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From the SnapMagic homepage, there is an InstaBuild drop down.	i make your design a	NY unitaria	1
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We'll use the SOP calculator because SOIC fixes pin pitch at 1.27 mm.	mance	Hi, bhurwitz	
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SnapMagic Burry For Oké Politagier Constant	print Generator
The datasheet pane is a nice touch.	SOP Footprint Builder
Each of these calculators is very similar, at least conceptually, to the KiCAD wizards.	No film an annual annua



SnapMagic Foot	print Genera	tor
RT4526 RICHTEK Outloo Dimension	A A B <td>Scrolling down, there is a complete table to fill out directly from the drawing, reducing the amount of guessing and math you have to do.</td>	Scrolling down, there is a complete table to fill out directly from the drawing, reducing the amount of guessing and math you have to do.
Be careful though – the letter identifiers may not match by default!	at 47 at 410 b 100	This adds a lot more flexibility over the KiCAD wizards.

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The pink is the mask layer, which is used for defining when soldermask openings are on the board. The other parts have those polygons too, but they're just the same size as the copper pad, which takes visual priority.



Here's a comparison between some dimensions between the SnapMagic model, on top, and the KiCAD model, on the bottom. It's close, but they're not not quite the same. Will one not work? We'd have to put them on a board and print a 1-to-1 copy of the board to find out. But the differences are small, so hopefully.

Either way, this is a great example of how error-prone and variable the footprint generation process can be! Especially given the variation between datasheets. It's critical to confirm your parts fit before sending your designs off for fab.



And with that, we conclude part 7C, and, as of spring 2024, the end of the PCB Design with KiCAD tutorial series.

If you have further design questions and you're on the Georgia Tech campus, feel free to stop by The Hive during open hours, normally during the semester from 11-6. There is usually a PI, MPI, or staff member available to help you, even if they don't know KiCAD. Design questions transcend software choices.

If you're not on campus, the internet is your friend here. For KICAD-specific questions, there are probably hundred of tutorials on KiCAD, especially the basics we've covered in this series, the forums are quite active, and the documentation is pretty good (though not complete). For design-specific questions, well, you'll probably be doing a lot of trial and error. Make some small boards to demo the concepts you're working on, read a lot, and decide how much error is tolerable to your system.

Design is a never-ending topic, and there is always more to learn.

Thanks for watching, and good luck!